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1 An object-oriented framework for interactive data graphics

Robert L. Young

December 1987 **ACM SIGPLAN Notices , Conference proceedings on Object-oriented programming systems, languages and applications**, Volume 22 Issue 12

Full text available: 📄 pdf(1.44 MB)   Additional Information: full citation, abstract, references, citings, index terms

> Ida is an object-oriented framework for interactive data graphics. It can be used for independent data examination or integrated into application user interfaces. Ida's model of graphics is composed of five basic elements: Presentations, Assemblies, Data Sources, Data Displays, and Scales. Presentations and assemblies address display layout. A sharp distinction is maintained between drawing and the management of already drawn images. Data sources are respon ...

2 Automatic compilation to a coarse-grained reconfigurable system-opn-chip

Girish Venkataramani, Walid Najjar, Fadi Kurdahi, Nader Bagherzadeh, Wim Bohm, Jeff Hammes

November 2003 **ACM Transactions on Embedded Computing Systems (TECS)**, Volume 2 Issue 4

Full text available: 📄 pdf(687.52 KB)   Additional Information: full citation, abstract, references, index terms

> The rapid growth of device densities on silicon has made it feasible to deploy reconfigurable hardware as a highly parallel computing platform. However, one of the obstacles to the wider acceptance of this technology is its programmability. The application needs to be programmed in hardware description languages or an assembly equivalent, whereas most application programmers are used to the algorithmic programming paradigm. SA-C has been proposed as an expression-oriented language designed to im ...

Keywords: Reconfigurable computing, SIMD, compilers

3 Loops in combinator-based compilers

Mitchell Wand

January 1983 **Proceedings of the 10th ACM SIGACT-SIGPLAN symposium on Principles of programming languages**

Full text available: 📄 pdf(461.62 KB)   Additional Information: full citation, abstract, references, citings, index terms

> In our paper [Wand 82a], we introduced a paradigm for compilation based on combinators. A program from a source language is translated (via a semantic definition) to trees of combinators; the tree is simplified (via associative and distributive laws) to a linear,

assembly-language-like format: the "compiler writer's virtual machine" operates by simulating a reduction sequence of the simplified tree. The correctness of these transformations follows from general results about the λ-calculus ...

Keywords: combinators, loops

4 Synthesis and Design Tools: A compiler framework for mapping applications to a coarse-grained reconfigurable computer architecture
Girish Venkataramani, Walid Najjar, Fadi Kurdahi, Nader Bagherzadeh, Wim Bohm
November 2001 **Proceedings of the 2001 international conference on Compilers, architecture, and synthesis for embedded systems**
Full text available: pdf(304.22 KB)   Additional Information: full citation, abstract, references, citings, index terms
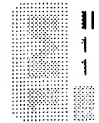
The rapid growth of silicon densities has made it feasible to deploy reconfigurable hardware as a highly parallel computing platform. However, in most cases, the application needs to be programmed in hardware description or assembly languages, whereas most application programmers are familiar with the algorithmic programming paradigm. SA-C has been proposed as an expression-oriented language designed to implicitly express data parallel operations. Morphosys is a reconfigurable system-on-chip arc ...

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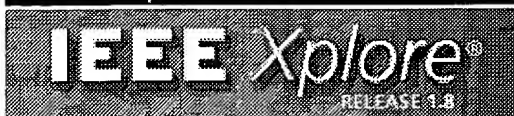
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Xiaorong Xiong; Hanein, Y.; Jiandong Fang; Yanbing Wang; Weihua Wang; Schwartz, D.T.; Bohringer, K.F.;
Microelectromechanical Systems, Journal of , Volume: 12 , Issue: 2 , April 200
Pages:117 - 127

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2   **Self-assembly of micro parts by controlling the environmental parameters**
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Pages:189 - 195

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3   **Modeling of capillary forces and binding sites for fluidic self-assemb**
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8   **Using temporal binding for connectionist recruitment learning over delayed lines**
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9   **Self-assembled monolayers of alkanethiols on nickel surfaces for lo level electrical contact applications**
Noel, S.; Houze, F.; Boyer, L.; Mekhalif, Z.; Delhalle, J.; Caudano, R.;
Components and Packaging Technologies, IEEE Transactions on [see also
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10   **Ge-laminated polarizer for 790-850-nm applications with extinctio ratios larger than 50 dB and insertion losses below 0.4 dB**
Takahashi, H.; Nagata, H.; Shiroishi, M.; Tamai, M.; Kataoka, H.;
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11   **A correct and complete algorithm for the generation of mechanica assembly sequences**
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12   **Geometric binding site design for surface-tension driven self-assem**
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14   **GEPI: genetically engineered proteins for inorganics as molecular erectors in nanotechnology and bio-technology**
Sarikaya, M.; Heidel, D.; Zareie, M.H.; Tamerler, C.; Baneyx, F.; Jen, A.;
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[Abstract]   [PDF Full-Text (276 KB)]   **IEEE CNF**

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